

```

;asmsyntax=asm68k
CPU      68000

miprogram   org      $1000
            add.w    d0,d1
            move.w   pepe(PC),d0
            trap     #15
            dc.w     0

pepe        dc.w     10

```

**En formato S-record** [https://en.wikipedia.org/wiki/SREC\\_\(file\\_format\)](https://en.wikipedia.org/wiki/SREC_(file_format))

```

S0030000FC
S10F1000D240303A00064E4F0000000AB7
S9030000FC

```

**Desglosado**

```

S0 03 0000 FC
S1 0F 1000 D240303A00064E4F0000000A B7
S9 03 0000 FC

```

**Por tanto**

```

;asmsyntax=asm68k
CPU      68000

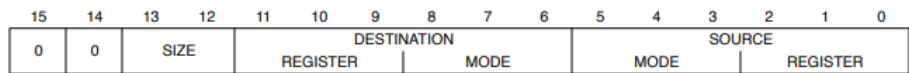
miprogram   org      $1000
            add.w    d0,d1
            move.w   pepe(PC),d0
            trap     #15
            dc.w     0

pepe        dc.w     10

```

**Interpretando move.w pepe(PC),d0**

MOVE < ea > , < ea >



move.w ?(PC),d0 | 303A |

- 01 — Byte operation
- 11 — Word operation
- 10 — Long operation

ORIGEN

Addressing Mode	Mode	Register
Dn	000	reg. number:Dn
An	001	reg. number:An
(An)	010	reg. number:An
(An) +	011	reg. number:An
-(An)	100	reg. number:An
(d <sub>16</sub> ,An)	101	reg. number:An
(d <sub>8</sub> ,An,Xn)	110	reg. number:An

Addressing Mode	Mode	Register
(xxx).W	111	000
(xxx).L	111	001
#<data>	111	100
(d <sub>16</sub> ,PC)	111	010
(d <sub>8</sub> ,PC,Xn)	111	011

Addressing Mode	Mode	Register
Dn	000	reg. number:Dn
An	—	—
(An)	010	reg. number:An
(An) +	011	reg. number:An
-(An)	100	reg. number:An
(d <sub>16</sub> ,An)	101	reg. number:An
(d <sub>8</sub> ,An,Xn)	110	reg. number:An

Addressing Mode	Mode	Register
(xxx).W	111	000
(xxx).L	111	001
#<data>	—	—
(d <sub>16</sub> ,PC)	—	—
(d <sub>8</sub> ,PC,Xn)	—	—

DESTINO

```

1002 | 303A |
1004 | 0006 |

```